BONAM VENKATA CHALAMAYYA INSTITUTE OF TECHNOLOGY AND SCIENCE AMALAPURAM

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FACULTY PROFILE

Name of the faculty	**Dr. SIVA SANKARA PHANI TAMMIREDDY**	
Designation	Professor	
Date Of Joining	01/05/2024	
AY	2008-2024	
Responsibility in Committees	1.Academic Committee & Advisory Committee 2.Internal Quality Assurance Cell 3.Councelling Committee 4.Anti-Ragging Squad Member	
Courses handled/List of Instructional materials prepared	**Courses handled** 1.Electronic Devices and circuits 2. Pulse and Digital Circuits 3. Linear Integrated Circuits 4. Analog Communications 5.Very Large Scale Integration 6.Optical Communication 7.Digital Signal Processing 8.Digital Communication 9.Electronic Switching System 10.Switching Theory and Logic Design 12.Microprocessor and Microcontrollers **Course Files Prepared:** 1.Microprocessor and Microcontrollers 2. Digital Communication 3. Linear Integrated Circuits **Lab Manuals:** DC Lab Manual	
Innovative T/L methodologies	Used 11 Different Teaching Methodologies 1. White Board With Marker & Talk (WMT) 2.Power point Presentation(PPT) 3. Tutoring (T) 4. Discussion (D) 5. Brain Storming (BS) 6. Seminar (SEM) 7. Quiz (Q) 8.Demonistartion 9.Brain Storming 10.Problem Solving 11. NPTEL Video	
Professional Memberships	1.IETE-M-210659	

Research Publications (Paper/Poster/book/book chapters/citations /etc)	[1] R. Agilesh Saravanan,B.T.P.Madhav **Siva Sankara Phani T**, "A High Gain Dual Feed Microstrip patch Antenna with Modified H-Slot for ka-and[28/38]Application".*International Journal of Microwave and Optical Technology,Vol.19,NO.2,March2024 (SC0PUS)*

[2] **Siva Sankara Phani Tammireddy**, M.Durga Prakash "An energy-efficient reconfgurable accelerators in multi-core systems using PULP-NN".*Applied Nanoscience https://doi.org/10.1007/s13204-021-02069-y (SCI)*

[3] **T. Siva Sankara Phani**,M.Durga Prakash,"Implementation of FPGA Based MRPMA for high performance Applications". *International Journal of Engineering & Technology, 7 (1.5) (2018) 158-163, www.sciencepubco.com/index.php/IJET (SCOPUS)*

[4] **Siva Sankara Phani.T**, M.Durga Prakash"Cgra Modulo Scheduling For Achieving Better Performance And Increased Effiency",*Turkish Journal of Physiotherapy and Rehabilitation; 32(3) ISSN 2651-4451 | e-ISSN 2651-446X, www.turkjphysiotherrehabil.org*

[5] **Siva Sankara Phani. T**,M. Durga Prakash "Ultra-Low Energy-Efficient Programmable Array Accelerator and Compiling Flows for the Near Sensor",*Annals of R.S.C.B., ISSN:1583-6258, Vol. 25, Issue 4, 2021, Pages. 4919 – 4942. http://annalsofrscb.ro*

[6] **T Siva Sankara Phani**, M Durga Prakash, K Hari Kishor & M Sujatha," Design And Implementation Of High Speed and Low Latency NoC Using Multigrained Reconfigurable and Parallel Mapping Architecture on FPGA Vol 73/No.10/Oct 2017 PONTE Journal

[7]T Siva Sankara Phani,B Ananda Krishna and Ranjan K. Senapathi,"Survey on Multigrained Reconfigurable Architecture using Parallel mapping Method" IJST Vol10(6) ISSN: 0974-5645,Feb 2017

[8] K Venkata Ramana, **T Siva sankara Phani** & T Saran Kumar,"IoT Based Home Automation System using Raspberry Pi" ISSN 2321 3361 ,2017IJESC Vol 7, Issue No 5

[9] Naga Lakshmi Balla , T S S Phani & A Pravin," An Optimized Design Of Area delay Power Efficient Architecture for Reconfigurable FIR Filter" Vol 5 ISSUE 12,Dec 2016 IJSETR

[10] S Kagitha, **T S S Phani** & A Pravin," Sensor Network based Thermal power Plant Inter lock Control and Remote Monitoring system",ISSN:2319-8753 Vol 3 ,Iss 12,2014   IJIRSET

[11] **T.S.S.Phani** and A.Pravin, "*control system for laser guidance based deflectable nose bullet,*" IJAEST : ISSN 2230-7818 ,issue 2011 (Volume No 10 , Issue No 2,245-248)

[12] P.Daniel, K.Rajasekhar, K.Ravikumar and **T.Phani Siva Sankar,** "*Tousch screen based electronic voting system*" International Journal of Engineering research & Technology(IJERT), ISSN 2278-0181,Vol.No 1,Issue 7,September-2012.

[13] Gummarekula Sattibabu, CH.Sridevi,**T.Siva Sankar Phani** and S.Ganika Sridevi , "Design of velocity-measuring system with Doppler radar concept and FFT algorithm based on ARM processor for traffic safety ", International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE), ISSN: 2278-909X Volume 1,Issue 4,October 2012.

[14] **T.S.S.Phani and** A Pravin "*FPGA Implementation of laser guided nose bullet", International Journal of Electronics and Communication technology(IJECT)*.Volume-2,SP-1,December 2011 |

Projects guided	2023-2024	1.Design & Comparison Of Quarter Wave And Direct Fed Sierpinski Carpet Fractal Antenna
	2022-2023	1.Automated Mopping of Solar Panel Based on Light Intensity 2.An Efficient and Enhanced Hybrid Parallel Prefix Adder for High Throughput Applications
	2021-2022	1.Foot Step Power Generating System 2.Wearable Taint Warner
	2020-2021	1.Robust digital Water Marking using Singular Value Decomposition
	2019-2020	1.Accident Prevention system using Body Movements 2.Heart Rate Profile Monitoring system
List of Working models / Products developed / Incubation/start – ups & List of projects received seed money	8	
List of projects received seed money	INR 56,000	
List of NPTEL courses certification done	1.Microprocessors and Interfacing[NPTEL] 2.Outcome based pedagogic principles for effective Teaching[NPTEL]	
Faculty Development programs attended /Organized /resource person	[1] FDP on" Embedded system Development using ARM mbed & Cypress PSoC Platform" from 1st – 6th March2017 at BVC Engineering College,Odalarevu [2] Two day National level Seminar on " Emerging Trends in Wireless Communications" from 18th and 19th March,2016 at BVC Engineering College,Odalarevu [3] A Three Day Workshop on "CMOS Digital IC Design using Mentor Graphics EDA Tools(WCDICD) from 21st -23rd March,2016 [4] A Four Day Workshop on "Research Methodology" from 8th – 11th July ,2014 At K L University Vijayawada	

	[5] WHRP-2K13, One day work shop on" Healthy Research Practices in Basic and Advanced Investigation",15th march-2013 [6] Two day workshop on "VLSI DESIGN", 16th & 17th, March2012,CBIT,Hyderabad [7] WERTS-2011, Two day National level workshop on "Embedded Real Time operating Systems", 22nd & 23rd Jan.2011, BVCEC, Odalarevu. [8] PEDAGOGY- A One day work shop on"Micro-teaching & Methodologies",22nd March,2009,BVCEC , Odalarevu [9] One day work shop on "IMAGE PROCESSING & MATLAB",1st December ,2009,BVCEC ,Odalarevu
Conferences/Seminars/Workshop attended/ organized	[1] T S S Phani, "Design and performance Analysis of 11-T SRAM Cell with BTI Reliability".Proceedings of the Second International Conference on Automation, Computing and Renewable System [ICACRS-2023], ISBN:979-8-3503-4022-8 [2] T.Siva Sankar Phani, M.Durga Prakash" Partial dynamic reconfiguration framework for FPGA: A survey with concepts, constraints and trends", *Materials Today: Proceedings, https://doi.org/10.1016/j.matpr.2021.01.851 2214-7853/ 2021 Elsevier Ltd* [3] NCETC-2K11, "Emerging Technologies in Electronics and Communications", 3rd &4th Dec.2011, [4] ICACCBIE – 2011 International Conferences on Advanced in Computer science, Communication And Bio Instrumentation Engineering during 8th and 9th Dec,2011
Ph.D enrolled/ awarded / guided	Ph.D awarded in VLSI at K L University
Industrial visits / Trainings / Internships organised	Yes
List of In-house/ Funded / Consultancy activities for R&D projects/documentation	NO
International fellowship for advanced studies/research	NO
Previous Experience	Worked as a Asst.Prof, Assoc.Prof & Professo in BVC Engineering college,Odalarevu, for the past 16 years