BR23-CSE II YEAR I SEMESTER SYLLABUS

II Year I Semester

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DIGITAL LOGIC & COMPUTER ORGANIZATION (23ES3T04)
(Common to CSE, IT Branches)

Course Objectives:

The main objectives of the course is to

- provide students with a comprehensive understanding of digital logic design principles and computer organization fundamentals
- Describe memory hierarchy concepts
- Explain input/output (I/O) systems and their interaction with the CPU, memory, andperipheral devices

COURSE OUTCOMES:

- Recall number system, and complements and Understand the general concepts in digital logic design. Remember (L1)
- Illustrate the concept of Sequential logic design, operations of flip-flops, registers and counters. Understand(L2)
- Apply algorithm for arithmetic operations. Apply(L3)
- Demonstrate memory organization methods to arrange and manage memory in computers. Apply(L3)
- Interpret Input/output organization methods for efficient mode of communication between central system and outside environment. Analyze(L4)

Dr.N.Rama Krishnaiah, Professor of CSE,UCEK & Control of Examination JNTUK, kakinada.	Dr.C.Krishna Mohan, Professor of CSE,IIT, Kandi, Hyderabad.	Dr.P.Radha Krishna, Professor of CSE,NIT, Warangal	Mr.Rajesh Bobburi Chief Operating Officer, HighQ Labs Private Limited, Rajahmundry	Dr.Lakshmi Haritha Medida, Associate Professor, R.M.K.Engineering College,Kavaraipettai ,Tamilnadu	Dr.K.Srinivas Professor & HoD Department CSE, B.V.C.I.T.S, Batlapalem

UNIT – I:

Data Representation: Binary Numbers, Fixed Point Representation. Floating Point Representation. Number base conversions, Octal and Hexadecimal Numbers, Complements, Signed binary numbers, Binary codes

Digital Logic Circuits-I: Basic Logic Functions, Logic gates, universal logic gates, Minimization of Logic expressions. K-Map Simplification, Combinational Circuits, Decoders, Multiplexers

UNIT – II:

Digital Logic Circuits-II: Sequential Circuits, Flip-Flops, Binary counters, Registers, Shift Registers, Ripple counters

Basic Structure of Computers: Computer Types, Functional units, Basic operational concepts, Bus structures, Software, Performance, multiprocessors and multi computers, Computer Generations, Von- Neumann Architecture.

UNIT – III:

Computer Arithmetic : Addition and Subtraction of Signed Numbers, Design of Fast Adders, Multiplication of Positive Numbers, Signed-operand Multiplication, Fast Multiplication, Integer Division, Floating-Point Numbers and Operations

Processor Organization: Fundamental Concepts, Execution of a Complete Instruction, Multiple-Bus Organization, Hardwired Control and Multi programmed Control

UNIT – IV:

The Memory Organization: Basic Concepts, Semiconductor RAM Memories, Read-Only Memories, Speed, Size and Cost, Cache Memories, Performance Considerations, Virtual Memories, Memory Management Requirements, Secondary Storage

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UNIT – V:

Input/Output Organization: Accessing I/O Devices, Interrupts, Processor Examples, Direct Memory Access, Buses, Interface Circuits, Standard I/O Interfaces

Textbooks:

1. Computer Organization, Carl Hamacher, ZvonkoVranesic, SafwatZaky, 6th edition,McGraw Hill
2. Digital Design, 6th Edition, M. Morris Mano, Pearson Education.
3. Computer Organization and Architecture, William Stallings, 11thEdition, Pearson.

Reference Books:

1. Computer Systems Architecture, M.Moris Mano, 3rdEdition, Pearson
2. Computer Organization and Design, David A. Paterson, John L.Hennessy, Elsevier
3. Fundamentals of Logic Design, Roth, 5thEdition, Thomson

Online Learning Resources:

1. https://nptel.ac.in/courses/106/103/106103068/
2. https://cse11-iiith.vlabs.ac.in/

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