



# BONAM VENKATA CHALAMAYYA INSTITUTE OF TECHNOLOGY & SCIENCE

(An Autonomous Institution)

(Approved by AICTE, New Delhi, Accredited by NAAC 'A' Grade Permanently Affiliated to JNTUK, Kakinada)



AMALAPURAM 533 201, Dr.B.R.AMBEDKAR KONASEEMA DT, ANDHRA PRADESH

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BVCITS/2025/CE/I P.G/MID-I

Date: 17 -11-2025

## TIME TABLE

The students of I M.Tech I Semester (BR25) are hereby informed that their MID - I examinations are scheduled as per the given timetable.

**TIMINGS: 10.10 AM – 12.10 AM**

BRANCH	DATE & DAY				
	24-11-2025 Monday	25-11-2025 Tuesday	26-11-2025 Wednesday	27-11-2025 Thursday	28-11-2025 Friday
<b>M.Tech_CSE</b>	Data Structures and Algorithm Analysis (25CSE1T01)	Advanced Dataware Housing and Data Mining (25CSE1T02)	Mathematical foundation of computer science (25CSE1T03)	<b>Professional Elective I</b> Advanced Software Engineering (25CSE1D04)	<b>Professional Elective II</b> Agile Methodologies (25CSE1D07)
<b>M.Tech_VLSI</b>	Digital CMOS Circuit Design (25VL1T01)	Analog CMOS Circuit Design (25VL1T02)	MoS Device Physics (25VL1T03)	<b>Professional Elective I</b> VLSI Technology (25VL1D01)	<b>Professional Elective II</b> VLSI Architectures (25VL1D02)

**Note 1:** If any clashes in the dates kindly inform us. All the mid question papers are to be send to [bvtsautonomousexamcell@gmail.com](mailto:bvtsautonomousexamcell@gmail.com) days in advance to the exam date. Template will be given to prepare the question paper. Mid exams are to be conducted in the scheduled dates only, no mid exam is to be reconducted for any student. Mid marks are to be posted within **3 days** of the completion of their Mid examination.

CC to: Principal

CC to HODs: ECE / CSE / AO / LIB / ACCOUNT SECTION / EX / TNP / TRANSPORT

CC to: Web Site/MNB/Steno/ Please Circulate the information among all related class rooms.



Controller of Examinations  
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BVC Institute Of Technology  
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